

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A surface-mountable miniature luminescent diode ~~and/or~~ or photodiode ~~with~~ comprising:  
  
a chip package which has a leadframe (16), and  
  
a semiconductor chip (22) which is arranged on, and is in electrical contact with, the leadframe (16) ~~and is in electrical contact with it~~ and which contains an active, radiation-emitting and/or radiation-receiving region,  
  
wherein the leadframe (16) is formed by a flexible multi-layered sheet (12, 14) that comprises a metal foil and a plastic film, the plastic film being arranged on, and connected to, the metal foil.
2. (Cancelled).
3. (Currently Amended) The surface-mountable miniature luminescent diode ~~and/or~~ or photodiode as claimed in claim [[2]] 1, wherein the plastic film (14) is adhesively bonded to the metal foil (12).
4. (Currently Amended) The surface-mountable miniature luminescent diode ~~and/or~~ or photodiode as claimed in claim [[2]] 1, wherein the metal foil (12) comprises a first chip

connection region (18) and a second chip connection region (20), and in that the plastic film has openings (34, 36) in the regions arranged on these chip connection regions (18, 20).

5. (Currently Amended) The surface-mountable miniature luminescent diode ~~and/or~~ or photodiode as claimed in claim 4, wherein the semiconductor chip (22) comprises a first contact area (24) on the first chip connection region (18), and a second contact area (26) coupled to the second chip connection region (20).

6. (Currently Amended) The surface-mountable miniature luminescent diode ~~and/or~~ or photodiode as claimed in claim [[2]] 1, wherein the thickness of the metal foil (12) is less than 80  $\mu\text{m}$ , ~~in particular between 30  $\mu\text{m}$  and 60  $\mu\text{m}$  inclusive.~~

7. (Currently Amended) The surface-mountable miniature luminescent diode ~~and/or~~ or photodiode as claimed in claim [[2]] 1, ~~[[,]]~~ wherein the plastic film comprises an epoxy resin film (14).

8. (Currently Amended) The surface-mountable miniature luminescent diode ~~and/or~~ or photodiode as claimed in claim [[2]] 1, the thickness of the plastic film (14) is less than 80  $\mu\text{m}$ , ~~in particular between 30  $\mu\text{m}$  and 60  $\mu\text{m}$  inclusive.~~

9. (Currently Amended) The surface-mountable miniature luminescent diode ~~and/or~~ or photodiode as claimed in claim 1, wherein the semiconductor chip (22) is embedded in an encapsulating material (30).

10. (Currently Amended) The surface-mountable miniature luminescent diode ~~and/or~~ or photodiode as claimed in claim 1, wherein the leadframe (16) has footprint dimensions of approximately 0.5 mm × 1.0 mm or less.

11. (Currently Amended) The surface-mountable miniature luminescent diode ~~and/or~~ or photodiode as claimed in claim 1, wherein the luminescent diode (10) has a total thickness of approximately 400 μm or less, ~~preferably of approximately 350 μm or less.~~

12. (Currently Amended) A method for producing a surface-mountable miniature luminescent diode ~~and/or~~ or photodiode, comprising:

providing a leadframe from a flexible multi-layered sheet which has a first chip connection region and a second chip connection region, the flexible multi-layered sheet comprising a thin metal foil and a plastic film, the plastic film being arranged on, and connected to, the metal foil;

providing a semiconductor chip, which contains an active, radiation-emitting region and has a first contact area and a second contact area;

mounting the semiconductor chip with the first contact area on the first chip connection region of the leadframe;

connecting the second contact area to the second chip connection region of the leadframe;  
and

encapsulating the semiconductor chip with a transparent or translucent encapsulating material.

13. (Currently Amended) The method as claimed in claim 12, wherein the step of providing a leadframe comprises ~~providing and~~ punching ~~[[a]]~~ the thin metal foil in order to define the first and second chip connection regions.

14. (Currently Amended) The method as claimed in claim 13, wherein the step of providing a leadframe comprises ~~providing and~~ punching ~~[[a]]~~ the thin plastic film in order to define openings for the electrical connection of the semiconductor chip.

15. (Previously Presented) The method as claimed in claim 14 wherein the step of providing a leadframe comprises the adhesive bonding of the foil and the film.

16. (Previously Presented) The method as claimed in claim 12, wherein, in the encapsulating step, the encapsulating material is injection-molded, transfer-molded or sprayed onto the plastic film of the multi-layered sheet

17. (Previously Presented) The method as claimed in claim 12, wherein, in the encapsulating step, a runner is led through a plurality of chips arranged on the multi-layered sheet.

18. (Previously Presented) The method as claimed in claim 12, wherein the first and second chip connection regions of the leadframe are short-circuited and grounded in the steps of

mounting the semiconductor chip, connecting the second contact area and encapsulating the semiconductor chip.

19. (Previously Presented) The method as claimed in claim 12, wherein a plurality of chips arranged on the multi-layered sheet are tested for their functional capability after the encapsulating step and in that, for this purpose, the individual chips are electrically isolated when they are mounted.